because the address of n is calulated at the la t3, fib\_result instruction and if inserted nop before it, the address value hold by fib\_result willl increase, therefore when I divide by 4 and assume the result will be at address 0 of DMEM it will be wrong since the new address will be 0x00010004 instead of 0x00010000 therefore not at address of DMEM so take care.



